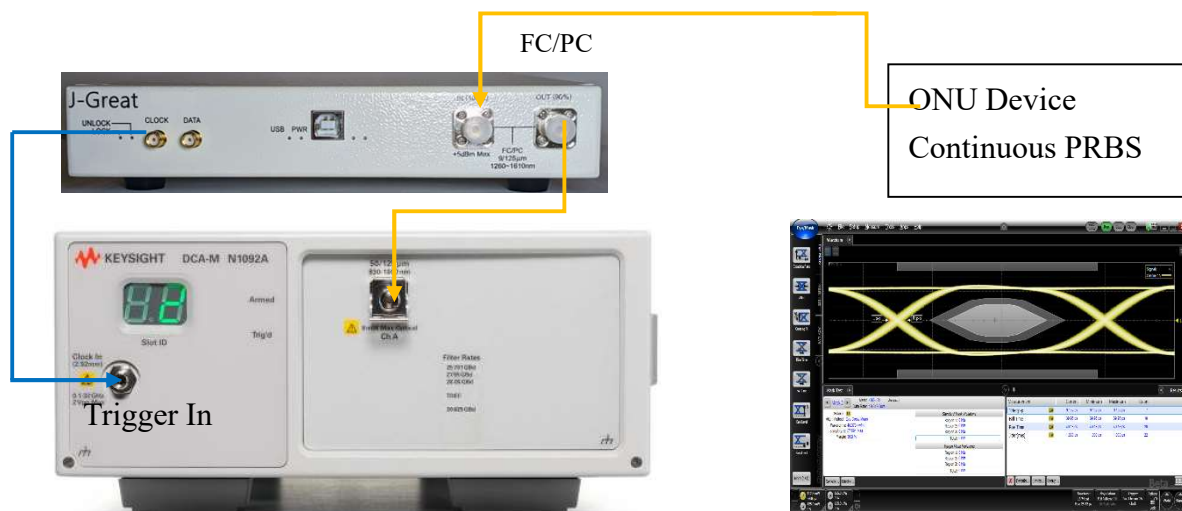


JC-F1000B 10Gbps Clock & Data Recovery Kit



The main application of Clock & Data Recovery Kit is used for in-line transmit Eye diagram measurement, most of sampling scope such as Keysight DCA-M provides Optical Eye pattern measurement capacity, but it needs embedded clock trigger to synchronous the measurement results.

J-Great CDR module is the solution to collaborate with the sampling scope that helps recover the clock corresponded to the data stream. With internal coupler, it can generate the clock signal in Electrical path and split the same optical signal into sampling scope at the same time.



System specification:

Clock recovery data rate

OC-192 SONET	9.95328 Gbps
10Gbps Ethernet	10.3125 Gbps
10Gbps Fiber Channel	10.51875 Gbps
ITU G.709	10.709 Gbps

Optical power range	+6 ~ -10dBm
Electrical Clock /Data out	400mVpp in typical
Electrical Impedance	50 Ohm in Typical
Optical Coupling ratio	10% (CDR) - 90% (Output)
Jitter Transfer Bandwidth	6MHz
Jitter Generation	10m UI rms in typical @ PRBS31-1
Optical In/ Out interface	FC/ PC 9/125um SMF
Electrical Output interface	SMA Female (Data/ Clock)
USB Power consumption	5V @ 400mA; 2W

Recommend with:

Agilent/ Keysight DCA Series Scope

Functional description

