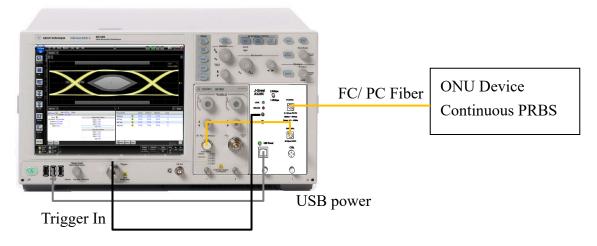
JC1225C GPON Clock & Data Recovery Kit



The main application of Clock & Data Recovery Kit is used for in–line transmit Eye diagram measurement, most of sampling scope such as Agilent 86100C/D + 86105C provides Optical Eye pattern measurement capacity, but it needs embedded clock trigger to synchronous the measurement results.

J-Great CDR module is the solution to collaborate with the sampling scope that helps recover the clock corresponded to the data stream. With internal coupler, it can generate the clock signal in Electrical path and split the same optical signal into sampling scope at the same time, The Loop bandwidth is to fit ITU standards that adopt for the jitter test requirement.



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System specification:

Clock recovery OC-24 1244.16M (1.25Gbps)/ OC-48 2488.32M (2.5Gbps)

Optical power range $+5 \sim -20 \text{dBm}$

Electrical Clock /Data out 400mVpp in typical Electrical Impedance 50 Ohm in Typical

Optical Coupling ratio 10% (CDR) - 90% (Output)

Jitter Transfer Bandwidth 750KHz @ OC-24, 1500KHz @ OC-48

Jitter Generation 10m UI rms in typical @ OC-48 PRBS23-1

Optical In/ Out interface FC/ PC 9/125um SMF

Electrical Output interface SMA Female (Data/ Clock)

USB Power consumption 5V @ 400mA; 2W

Digital IO Control 3.3V LVTTL GND/ Status Ping / Data Rate select Ping.

Extension Date rate option Optional 9.95328/ 10.3125Gbps (Please contact J-Great sales)

Recommend with:

Agilent/ Keysight 86100A/ 86100B/ 86100C/ 86100D Mainframe & 86103B/ 86105A/B/C Module

Functional description: 1.25Gbps/ 2.5Gbps Data Rate selection Clock Recovery FC/PC input for Lock/ unlock status J-Great GPON device IN (100%) LOCK (0) Clock signal output to connect UHLOCK (III) 86100X mainframe trigger in ana G FC/ PC output to connect DATAG 86105C Reference OE Data signal output for Optical converter to Electrical debugging purpose Power ON status Digital IO for Lock status monitoring and USB Type B adapter Data Rate selection for DC power input

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